

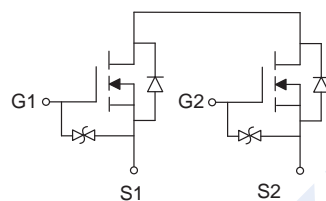
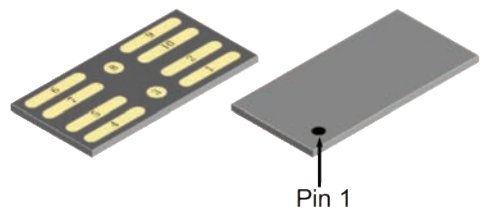
## 12V 2.4mohm Dual N-channel Trench MOSFET

## 2KK5138CSP

## ■ Features

- $V_{SS} = 12\text{ V}$
- $I_S = 13\text{ A}$
- Trench MOSFET technology
- Extremely Low  $R_{SS(ON)}$
- ESD HBM Class 2
- Common Drain Design

WLCSP - 2.92 X 1.43-10L



Equivalent circuit

■ Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Rating	Unit	
Source - Source Voltage	$V_{SS}$	12	V	
Gate-Source Voltage	$V_{GS}$	$\pm 8$		
Source Current - Continuous (Note 1)	$I_S$	$T_C = 25^\circ\text{C}$	13	A
		$T_C = 100^\circ\text{C}$	10	
Source Current - Pulsed (Note 2)	$I_{SM}$	52		
Power Dissipation	$P_D$	0.5	W	
Thermal Resistance. Junction to Ambient (Note 3)	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$	
Junction Temperature	$T_J$	150	$^\circ\text{C}$	
Storage Temperature Range	$T_{stg}$	-55 to 150		

Notes:

1. The max Source current rating base on silicon
2. Pulse Test: Pulse width  $\leq 300\text{ us}$ , Duty cycle  $\leq 2\%$
3. Mount on 1X1 inch 2oz FR - 4 PCB

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## ■ Electrical Characteristics (Tc = 25°C unless otherwise specified)

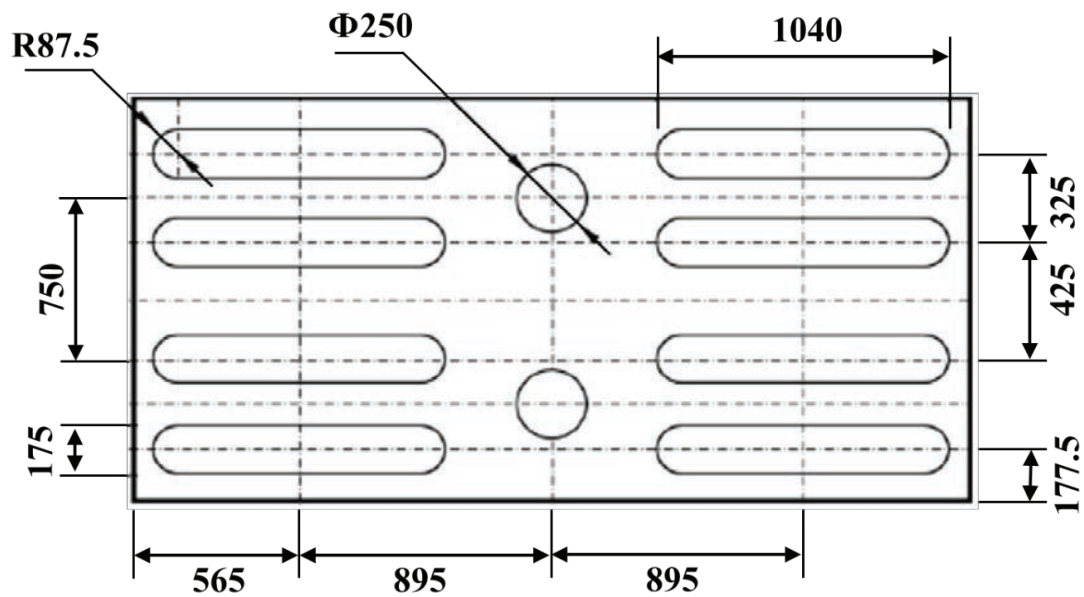
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Source-Source Breakdown Voltage	BV <sub>SS</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 250 μA	12			V
Zero Gate Voltage Source Current	I <sub>SS</sub>	V <sub>SS</sub> = 12 V, V <sub>GS</sub> = 0 V			1	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±8 V, V <sub>SS</sub> = 0 V			±10	
Gate Threshold voltage	V <sub>GS(TH)</sub>	V <sub>SS</sub> = V <sub>GS</sub> , I <sub>S</sub> = 1 mA	0.55	0.95	1.35	V
Source-Source on-state resistance	R <sub>SS(ON)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>S</sub> = 4 A		1.6	2.4	mΩ
		V <sub>GS</sub> = 3.8 V, I <sub>S</sub> = 4 A		1.8	2.7	
		V <sub>GS</sub> = 3.1 V, I <sub>S</sub> = 4 A		2.2	3.5	
		V <sub>GS</sub> = 2.5 V, I <sub>S</sub> = 4 A		3.5	5.5	
Forward Source to Source Voltage	V <sub>FSS</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 4 A		0.6	1.2	V
<b>Dynamic Characteristics</b>						
Input capacitance	C <sub>ISS</sub>	V <sub>SS</sub> = 10 V, V <sub>GS</sub> = 0 V, F = 1 MHz		3500		pF
Output capacitance	C <sub>OSS</sub>			450		
Reverse transfer capacitance	C <sub>RSS</sub>			400		
Gate resistance	R <sub>G</sub>	F = 1 MHz		1		KΩ
<b>Switching Characteristics</b>						
Turn On Delay Time	T <sub>D(ON)</sub>	V <sub>SS</sub> = 6 V, I <sub>S</sub> = 4A, V <sub>GS</sub> = 4.5 V, R <sub>G</sub> = 3 Ω		0.6		ns
Rise Time	T <sub>R</sub>			1.4		
Turn Off Delay Time	T <sub>D(OFF)</sub>			6.6		
Fall Time	T <sub>F</sub>			4		
Total Gate Charge	Q <sub>G</sub>	V <sub>SS</sub> = 6 V, I <sub>S</sub> = 4 A, V <sub>GS</sub> = 4.5 V		23		nC
Gate-Source Charge	Q <sub>GS</sub>			11		
Gate-Drain Charge	Q <sub>GD</sub>			5		

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## ■ WLCSP - 2.92 X 1.43-10L Package Outline Dimensions

## Bottom View



## Side View

